

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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L. McLaughlin  
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Applicant : Oscar E. Agazzi, et al.  
Application No.: Not Yet Assigned  
Filed : Concurrently Herewith  
Title : TIMING RECOVERY SYSTEM FOR A  
MULTI-PAIR GIGABIT TRANSCEIVER  
Docket No. : 47536/SDB/B600

## PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Post Office Box 7068  
Pasadena, CA 91109-7068  
January 21, 2002

Commissioner:

Prior to examination of the above-identified application, please enter the following amendments.

In The Specification:

Please amend the two paragraphs under CROSS-REFERENCE TO RELATED APPLICATIONS on page 2 as follows:

The present application is a continuation of U.S. Patent application Serial No. 09/437,721, filed November 9, 1999 which claims priority of the following provisional applications, the contents of each of which are herein incorporated by reference: Serial Number 60/107,874 entitled "Apparatus for, and Method of, Distributing Clock Signals in a Communications System" filed on November 9, 1998; Serial Number 60/108,319 entitled "Gigabit Ethernet Transceiver" filed on November 13, 1998; Serial Number